

TL4000 series

Model	TL4134E	TL4134B	TL4234B
Power	Power Source	USB bus-power (+5V)	
	Static Power Consumption	0.8W	
	Max Power Consumption	3W	
Hardware Interface	USB 3.0		
Timing Analysis (Asynchronous, Max. Sample Rate)	2GHz		
State Clock Rate (Synchronous, External Clock)	250MHz		
Storage	Conventional Timing, Transitional Timing		
Channels (Data / CLK / Analog / GND)	32 / 2 / 2 / 4		
Total Memory	4 Gb		8 Gb
Timing vs. Channels vs. Memory	Timing Analysis	Available channels (Conventional / Transitional Timing) - Memory per channel	
	2GHz	(8/7) - 512Mb	(8/7) - 1Gb
	1GHz	(16/14) - 256Mb	(16/14) - 512Mb
	500MHz	(32/28) - 128Mb	(32/28) - 256Mb
250MHz	(32/32) - 128Mb	(32/32) - 256Mb	
Channel to channel skew	< 1ns		
Threshold	Group	4 (ch0~7, ch8~15 & clk0, ch16~23, ch24~31 & clk1)	
	Range	+5V ~ -5V	
	Resolution	50mV	
	Accuracy	±100mV + 5%*Vth	
Input Voltage	Non-Destructive Operation	±30V DC, 12Vpp AC	
	Sensitivity	+10V ~ -10V	
		0.25Vpp @50MHz, 0.5Vpp @150MHz, 0.8Vpp @250MHz	
Impedance	Data channels	200KΩ//<7pF	
	Analog channels	20KΩ//<3pF	
Analog Inputs (2) (Protocol Analyzer)	Maximum (Non-destructive) Operation	-0.5V ~ +8V DC+AC peak	
	Resolution	0V ~ 4V	
	Sampling Rate	12 bits	
		250KHz	
Temperature	Operating / Storage	5°C~40°C (41°F~104°F) / -10°C~65°C (14°F~149°F)	
I/O port	Trig-In	TTL 3.3V level (Rising / Falling)	
	Trigger pulse approval	> 8 ns	
	Trig-Out	TTL 3.3V, Pulse Width	
	Ref. Clock Input	10MHz, Vpp=3.3 to 5V	
	Ref. Clock Output	10MHz, TTL 3.3V	
	Connector type	MCX jack / female	
Trigger	Resolution	500ps	
	Channels	32	
	States	16	
	Events	16	
	Pre / Post	Yes	
	Pass Counter	Yes (0~1048575 times)	
	Types	Channel, Pattern, Single / Multi Level, Width, Time-out, External	
	Bus I	BiSS-C, CAN2.0B/CAN FD, DP_Aux ¹ , HID over I2C, I2C, I2S, LIN2.2, SPI, UART (RS232), USB PD 3.0	
	Bus II	--- DALI, I3C, LPC, MDIO, Mini/Micro LED, MIPI RFFE, MIPI SPMI 2, Modbus, PMBus, Profibus, SMBus, SVI2, USB1.1	
	Bus III	--- eMMC 4.5, eSPI, MII, NAND Flash, RGMII, RMII, SD 3.0 (SDIO 2.0), Serial Flash (SPI NAND), SVID ³	
Protocol Analyzer/ Protocol Logger / Protocol Monitor	I	BiSS-C, CAN2.0B/CAN FD, DP_Aux ¹ , HID over I2C, I2C, I2S, LIN2.2, SPI, UART (RS232), USB PD 3.0	
	II	--- DALI, I3C, LPC, MDIO, Mini/Micro LED, MIPI RFFE, MIPI SPMI 2, Modbus, PMBus, Profibus, SMBus, SVI2, USB1.1	
	III	--- eSPI, MII, RGMII, RMII, SVID ³	
Software Features	Zoom In / Out	Yes	
	Language	English / Simplified Chinese / Traditional Chinese	
	Waveform Height	Adjustable	
	Zoom / Report Window	Yes	
	Quick Cursor-positioning	Yes	
	Import Label(s)	Yes	
	Quick Bus Decode Setup	Yes	
	Trigger / Auxiliary cursors	1/25	
	Data Logger	Saved to Hard Disk Drive	
	Bus Decode	1-Wire, 3-Wire, 7-Segment, A/D Mux Flash, AccMeter, ADC, APML, AVSBus, BiSS-C, BSD, BT1120, CAN 2.0B/FD, Close Caption, CODEC_SSI, DALI, DMX512, DP AUX ¹ , EDID, eMMC 5.1/MMC, eSPI, FlexRay, HD Audio, HDLC, HDQ, HID over I2C, I2C, I2C EEPROM, I2S (PCM, TDM), I3C, I80, IDE, IrDA, ITU-R BT.656 (CCIR656), JTAG, JVC IR, LCD1602, LED_Ctrl, LIN 2.2, Line Decoding, Line Encoding, Lissajous, LPC, LPT, Math, M-Bus, MDDI, MDIO, MHL CBUS, Microwire, MII, RGMII, RMII, Mini/Micro LED, MIPI CSI LP, MIPI DSI LP, MIPI RFFE, MIPI SPMI 2.0, Modbus, NAND Flash, NEC IR, PECL 3.0, PMBus, Profibus, PS/2, PWM, QEI, QI, RC-5, RC-6, RGB Interface, S/PDIF, SD 3.0 (SDIO 2.0), Serial Flash, Serial IRQ, SGPIO, Smart Card, SMBus (SBS, SPD), SMI, SoundWire, SPI, SPI-NAND, SSI, ST7669, SVI2, SVID ² , SWD, SWIM, SWP, UART (RS232), ULPI, UNI/O, USB 1.1, USB PD 3.0, Wiegand, ...	
Line Decoding	Biphase Mark, Differential-Manchester, Manchester (Thomas, IEEE802.3), Miller, Modified Miller, NRZI, ...		
Line Encoding	AMI(Standard, B8ZS, HDB3), Biphase Mark, CMI, Differential-Manchester, Manchester (Thomas, IEEE802.4), MLT-3, Miller, Modified Miller, NRZI, Pseudoternary, ...		
Dimension	L x W x H (mm ³)		
Lead Cable	(Data / CLK / Analog / GND)		
Grippers	A 40-pin lead cable (32 / 2 / 2 / 4)		
	40		

¹ Optional DP AUX adapter needed.

² Upon request ONLY by users who have signed CNDA with Intel, SVID decode supported by all TL4000 models.

³ Upon request ONLY by users who have signed CNDA with Intel, SVID trigger & PA supported by TL4234B ONLY.

Specifications marked in BLUE are different from TL3000 series.

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Acute TravelLogic

Logic Analyzer & Protocol Analyzer

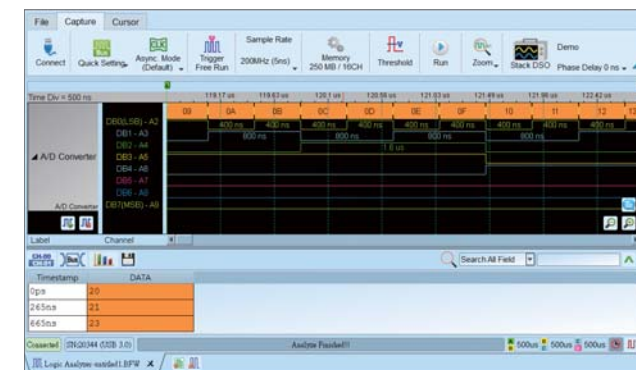


123 x 76 x 21 mm³

- PC-based
- USB 3.0 Interface
- 34 Channels
- 2 GHz Timing / 250MHz State Analysis
- 8Gb Memory (Max.)
- Data logger saved to hard disk drive
- Analog Inputs (2) for Protocol Analyzer
- Stacks with Acute or another DSO to form as an MSO
- Bus Decode : BiSS-C, CAN 2.0B/CAN FD, DP_Aux¹, DMX512, EDID, eMMC 5.0, eSPI, I²C, I²S, I3C, MII, MIPI DSI LP, NAND Flash, NEC IR, Profibus, SD 3.0 (SDIO 2.0), Serial Flash, SPI, SVID², SWD, UART (RS232), USB1.1, USB PD 3.0... (90+)
- Bus Trigger I : BiSS-C, CAN2.0B/CAN FD, DP_Aux¹, I2C, I2S, LIN2.2, SPI, UART, USB PD 3.0, ...
- Bus Trigger II : DALI, I3C, LPC, Mini/Micro LED, PMBus, Profibus, SMBus, SVI2, USB1.1, ...
- Bus Trigger III : eMMC 4.5, eSPI, MII, RGMII, RMII, NAND Flash, SD 3.0 (SDIO 2.0), SVID³, ...
- Protocol Analyzer I : BiSS-C, CAN2.0B/CAN FD, DP_Aux¹, I2C, I2S, SPI, USB PD 3.0, ...
- Protocol Analyzer II : DALI, I3C, LPC, Mini/Micro LED, Profibus, SMBus, SVI2, USB1.1, ...
- Protocol Analyzer III : eSPI, MII, RGMII, RMII, SVID³

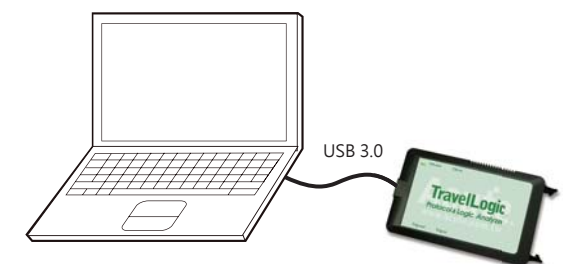
Model	Channels	Sample Rate	Memory	Bus Trigger	Protocol Analyzer
TL4134E	34	2GHz	4Gb	I	I
TL4134B	34	2GHz	4Gb	I, II	I, II
TL4234B	34	2GHz	8Gb	I, II, III	I, II, III

Software Window



System Requirements

- USB 3.0 port
- Win 7, Win 8, Win 10 (64 bit)
- PC RAM 16GB (recommended) or 8GB at least



Acute

PC-based T&M Instruments

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Protocol Analyzer:

It is hardware decoding, may log protocol data very long time if without waveforms.
Application timing: Preliminary protocol debug.

Support multiple protocols with different operating modes

Real-time data search

Stack with a DSO as an MSO in logic analyzer mode

The screenshot shows the software interface with several key features highlighted:

- Protocol Analyzer Mode:** Indicated by a red box around the 'Protocol Analyzer' button in the top toolbar.
- Real-time data search:** A red box highlights the search bar and 'Search All Field' dropdown.
- Stack DSO:** A red box highlights the 'Stack DSO' button in the top toolbar.
- Real-time data statistics:** A red box highlights the 'Statistics' tab in the Navigator panel.
- Hide items for easy view:** A red box highlights the 'Hide Items' button in the Navigator panel.
- Protocol report:** A red box highlights the 'Protocol report' label above the waveform.
- Show waveforms with bus decodes:** A red box highlights the waveform area with overlaid bus decodes.



Protocol Analyzer

Show real-time protocol data
Application timing: massive protocol data with some idles in between



Protocol Logger

Like data logger, save massive data into SSD hard drive
Application timing: massive protocol data



Protocol Monitor

Like dash cameras, record protocol data by the device's memory only
Application timing: trigger event only happens in very long time

Packing List :



Software and Manual Download links at: <http://www.acute.com.tw>

Logic Analyzer:

Capture digital waveforms and support bus decodes. Able to stack with a DSO to form as an MSO.

Provides multiple storage modes, users could select to have long time recording or precision acquisition.

LA Storage mode

The diagram illustrates four storage modes for the Logic Analyzer:

- Conventional Storage:** Signal Rate 250MHz. Uses LA Device RAM.
- Transitional Storage:** Signal Rate 250MHz. Uses LA Device RAM.
- Streaming to PC RAM:** Signal Rate ≤ 250MHz (Depends on PC's performance). Uses PC RAM.
- Streaming to PC HDD:** Signal Rate ≤ 250MHz (Depends on PC's performance). Uses PC HDD.

A timeline at the bottom indicates 'Short time' and 'Long time' recording capabilities.

Flow chart bus triggers :

The screenshot shows the 'Clause Trigger' configuration interface:

- Clause Trigger:** A flow chart with states (State 1, State 2, State 3) and a counter (Counter 1).
- State 1:** Configured with 'Event 1' and '7-Bit Addressing'.
- Detail parameters for each state:** A red box highlights the 'Data' section, showing options for 'Any Position' or 'Fix Offset' and data value settings.
- Power trigger for serial bus, 8-states flow chart setting with Counter/Timer:** A red box highlights the 'Counter 1' and 'Trigger' options.

Quick View

Right-click and drag on the clock waveform to see the frequency and the number of transitions

The screenshot shows the software interface with several key features highlighted:

- Clear setting:** A red box highlights the 'Quick Setting' button.
- Single or repetitive captures:** A red box highlights the 'Run' and 'Repeat' buttons.
- Fast DSO stack setting:** A red box highlights the 'Stack DSO' button.
- Display digital and analog waveforms at the same phase:** A red box highlights the waveform area.
- User mark:** A red box highlights the 'Note 1' window with 'Acute Note' text.
- Report window:** A red box highlights the 'Report window' label at the bottom.